

Fig. 1
Prior Art

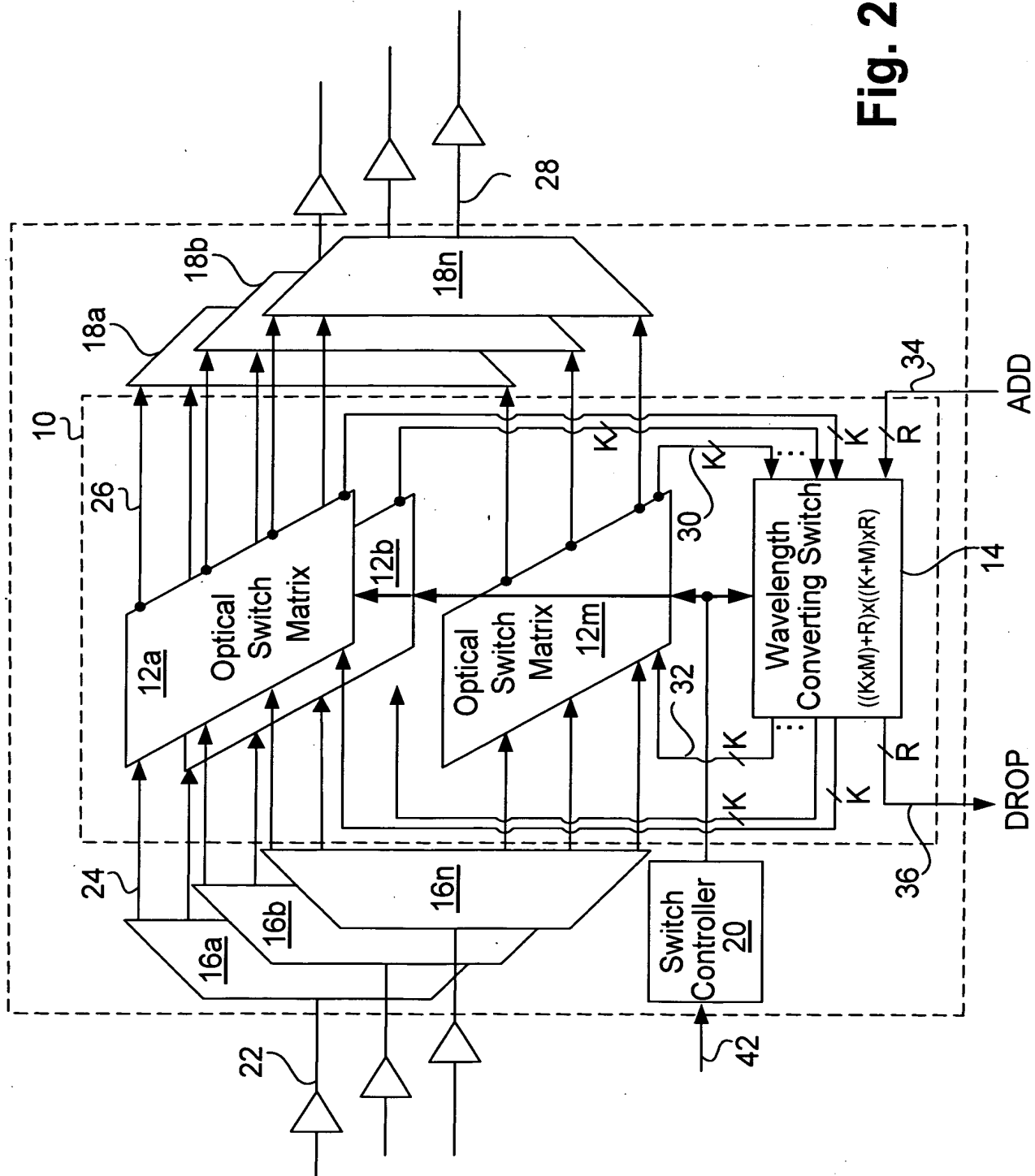


Fig. 2

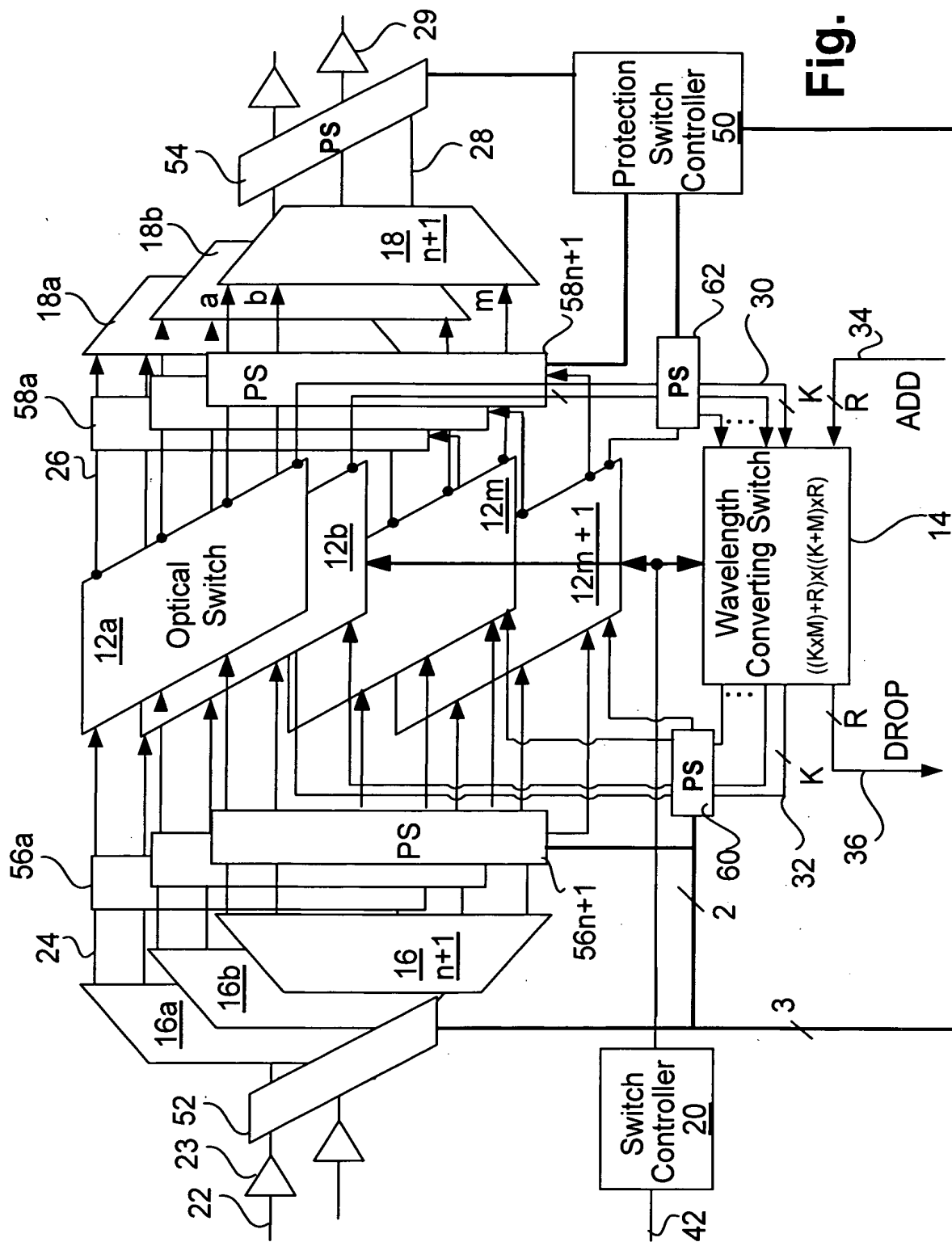


Fig. 3

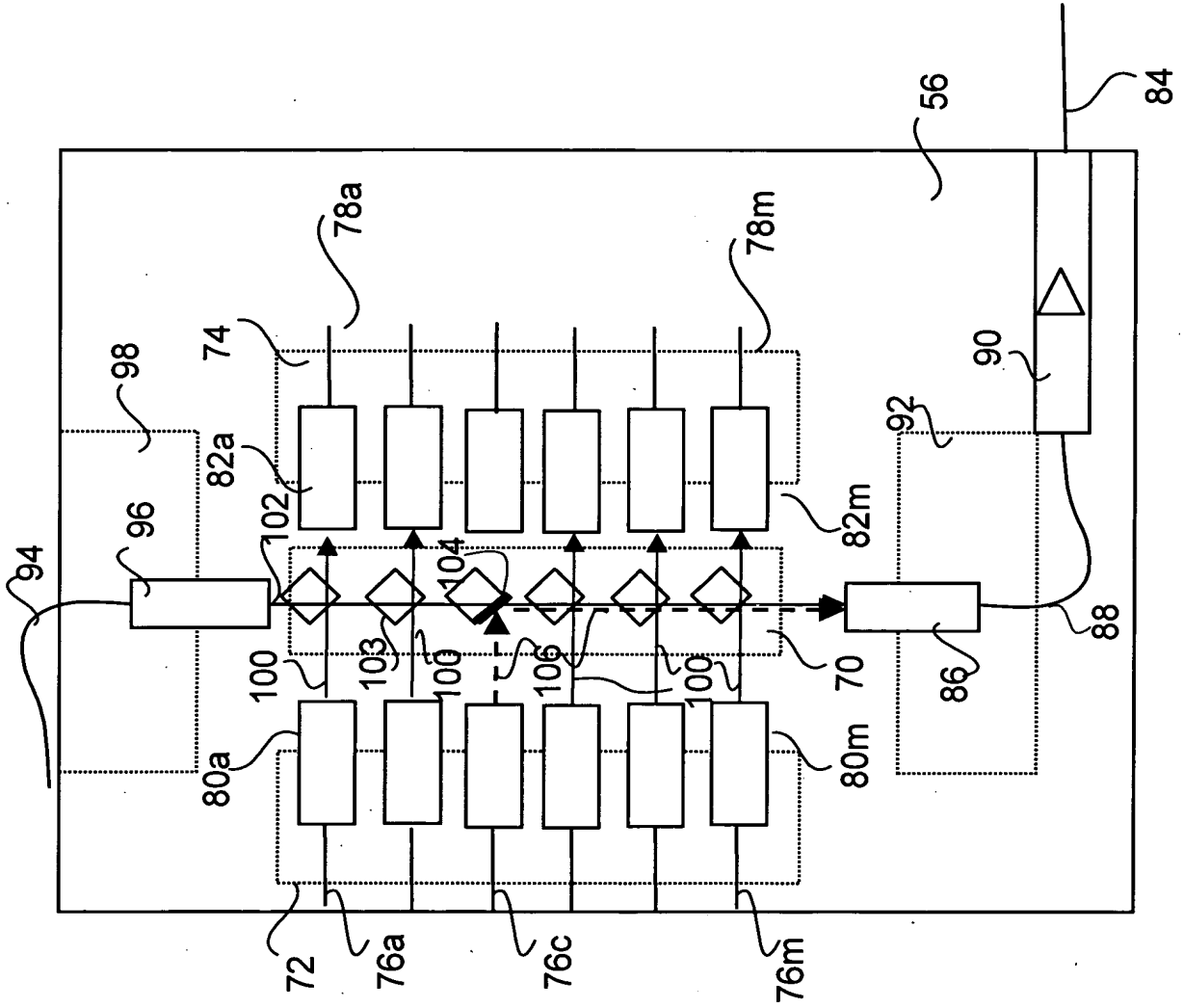
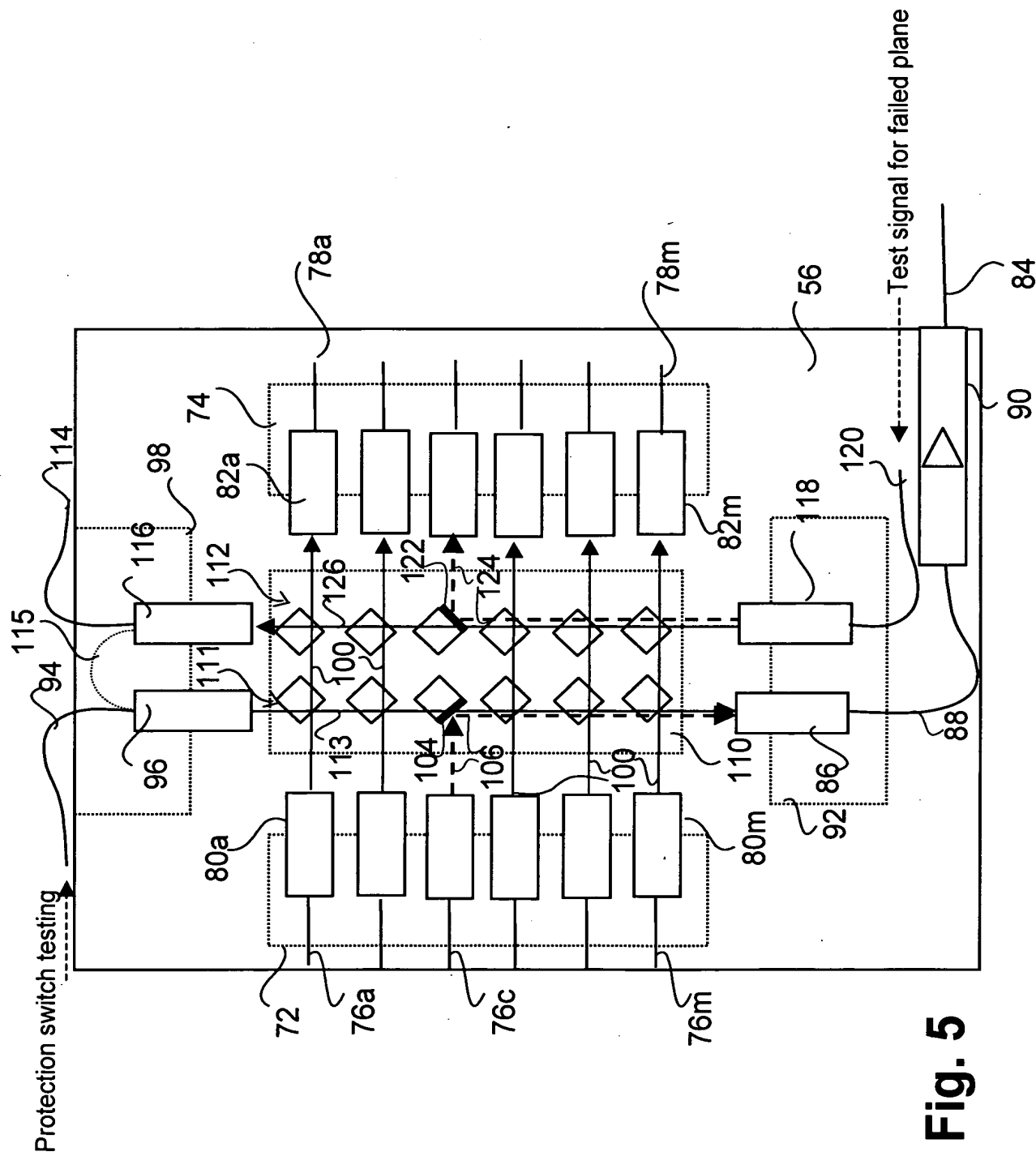


Fig. 4



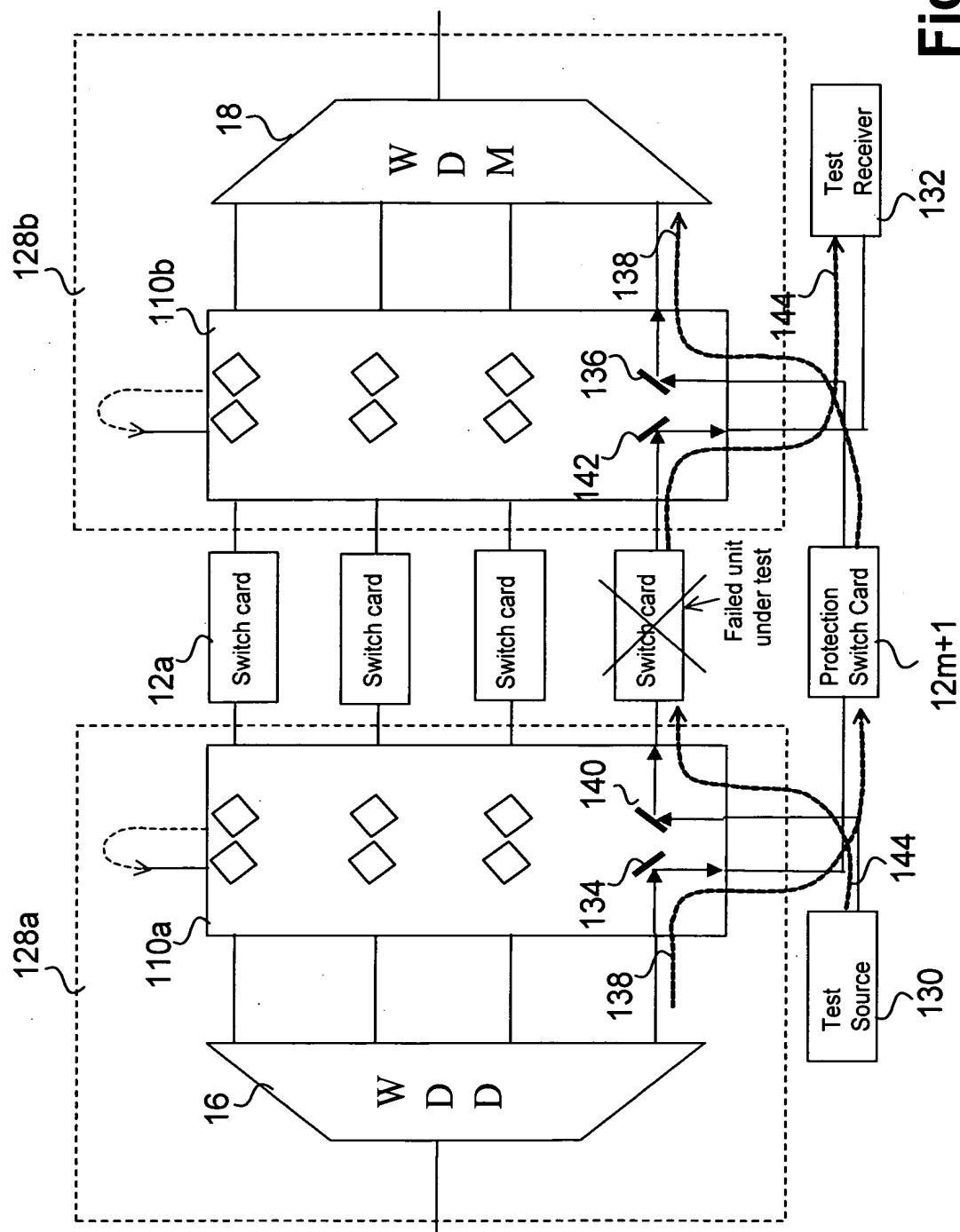
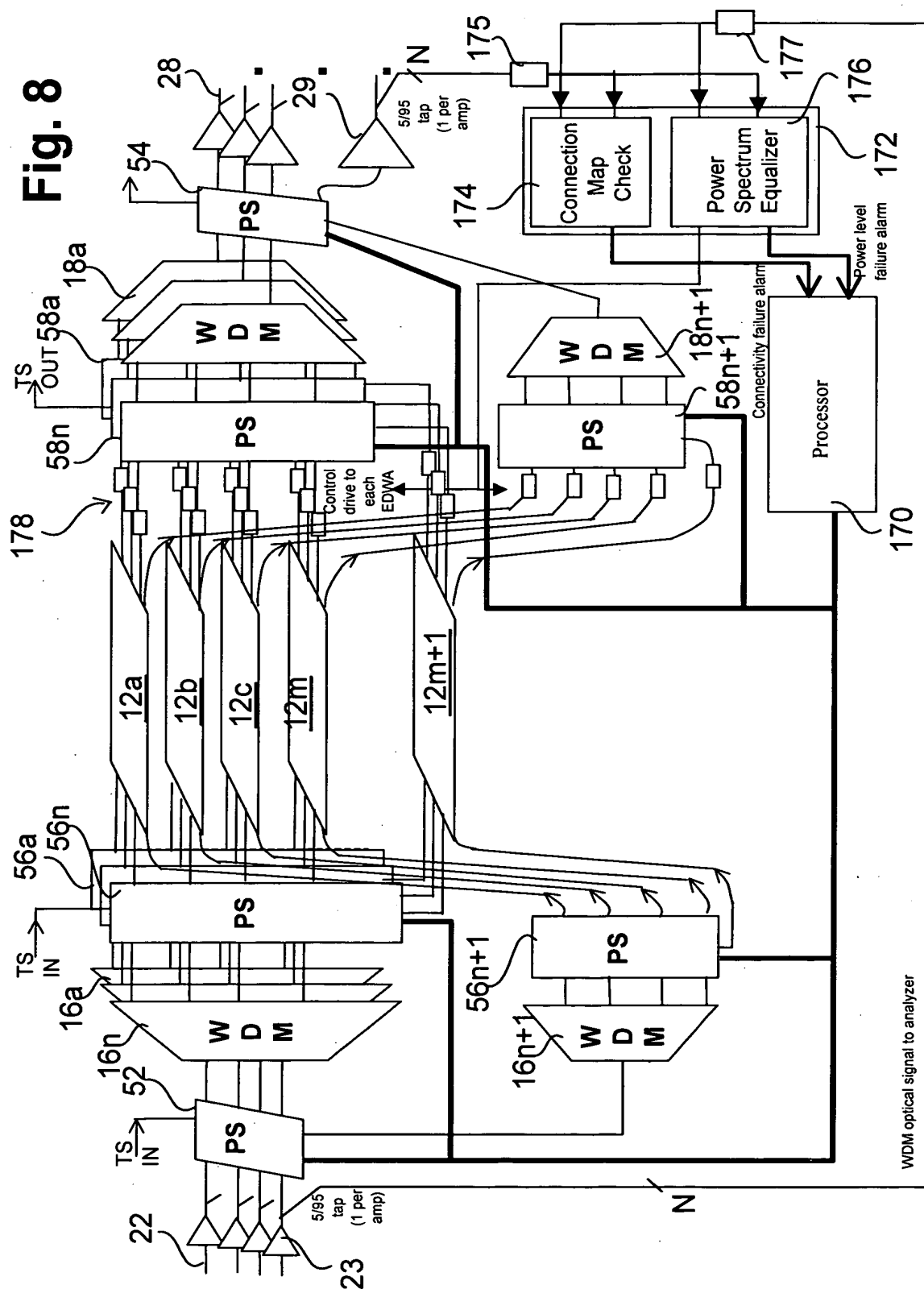


Fig. 6



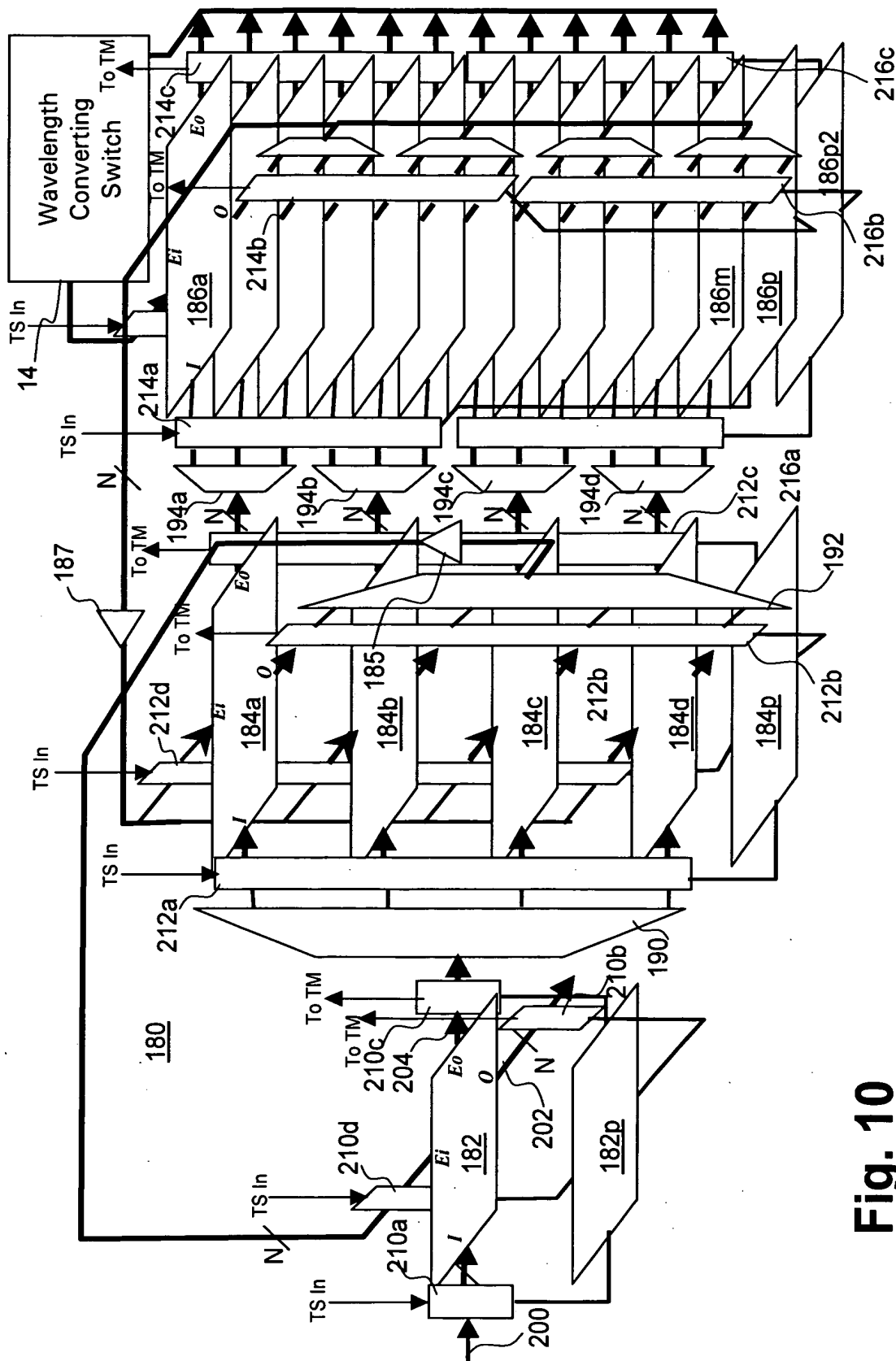


Fig. 10

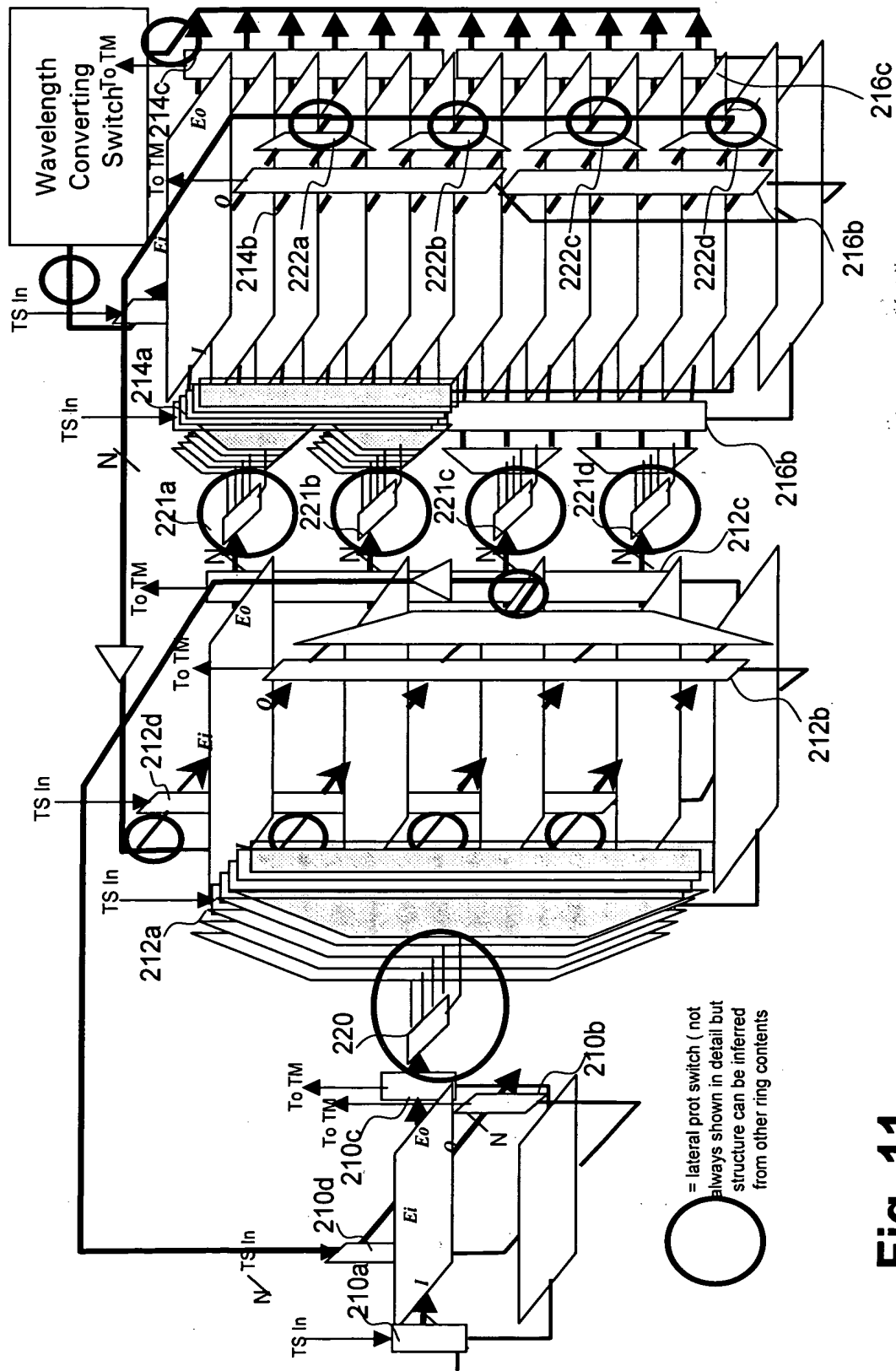


FIG. 12 is a schematic diagram of a 6-port MEMS structure and available optical paths.

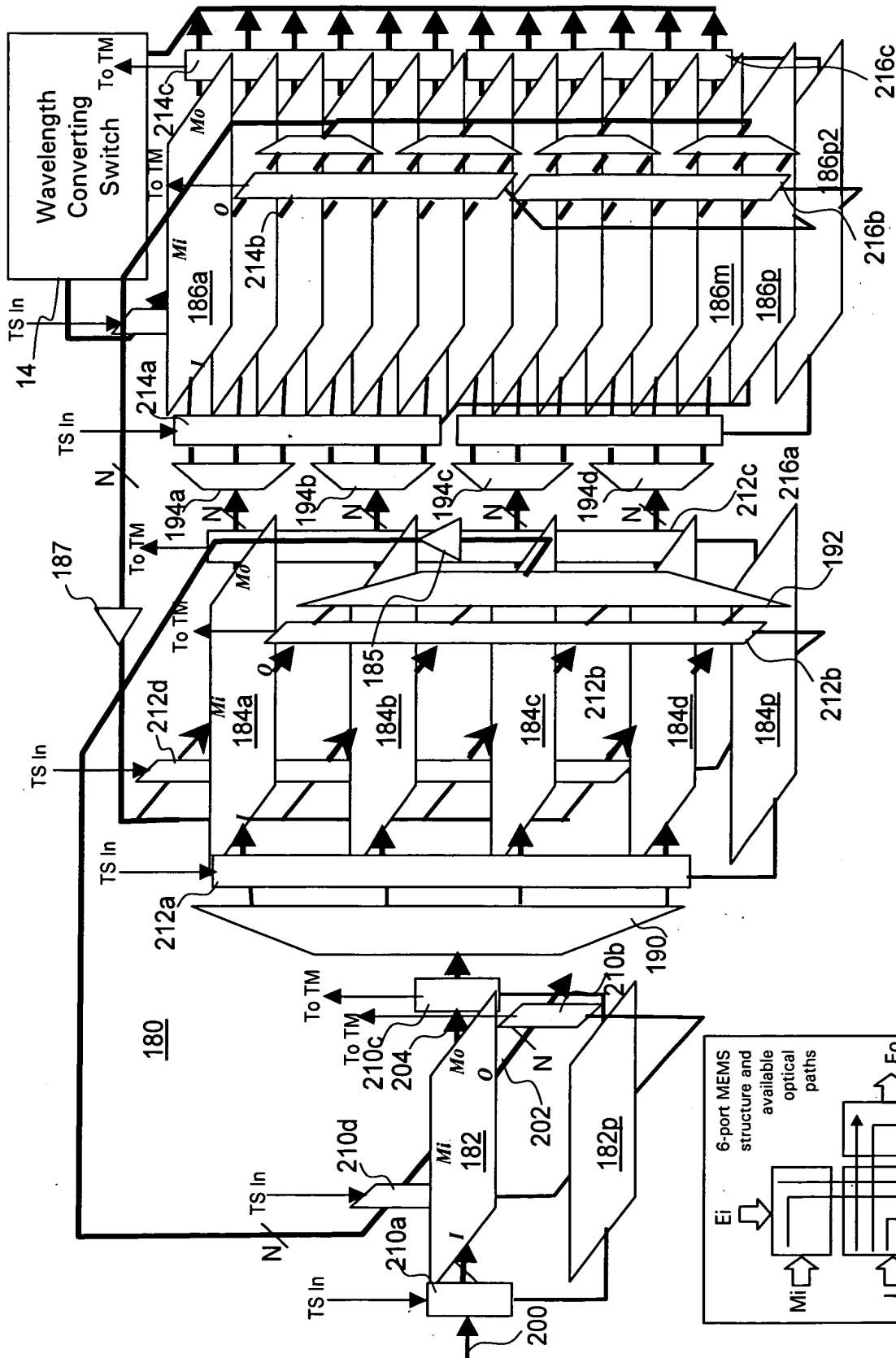


Fig. 12

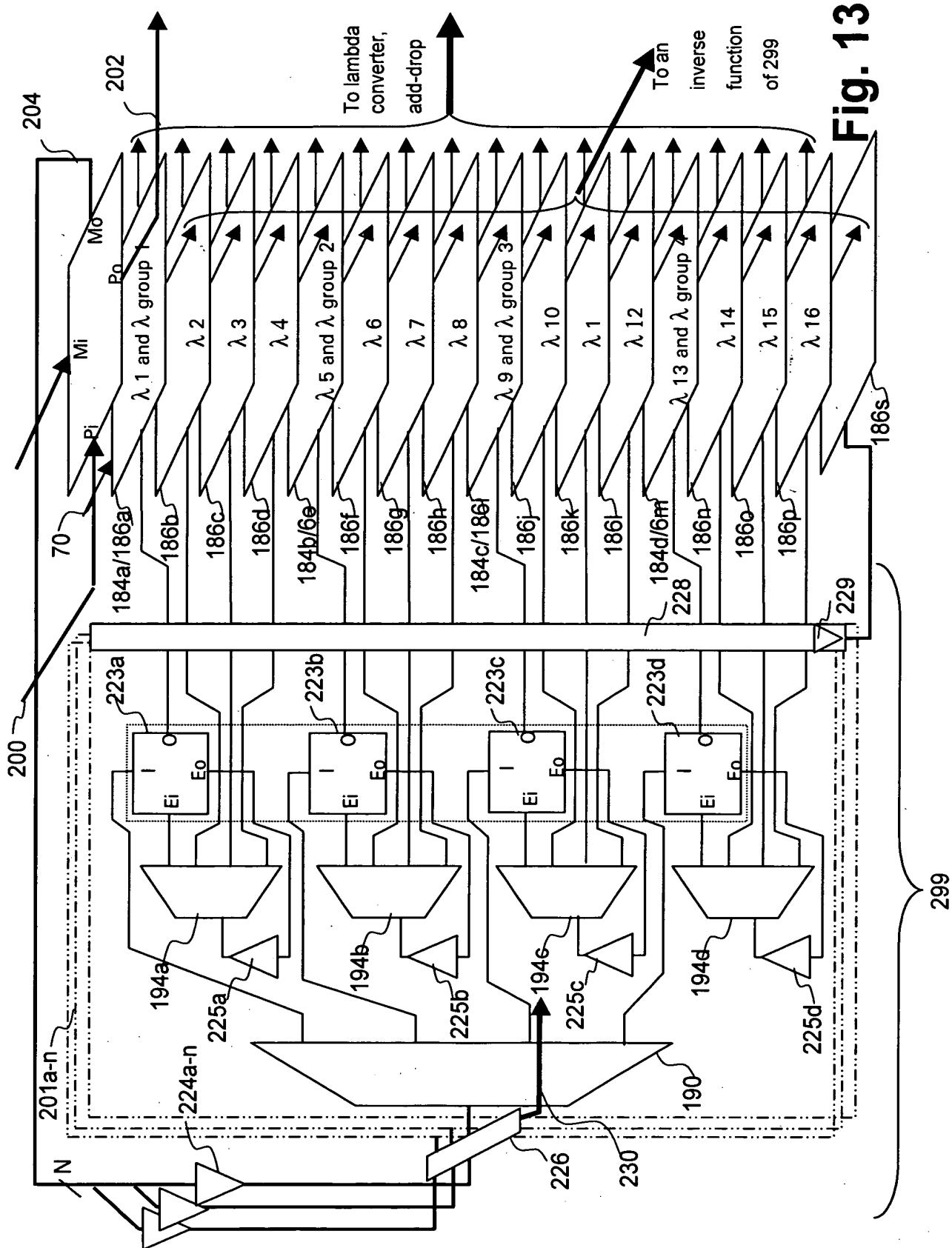


Fig. 13

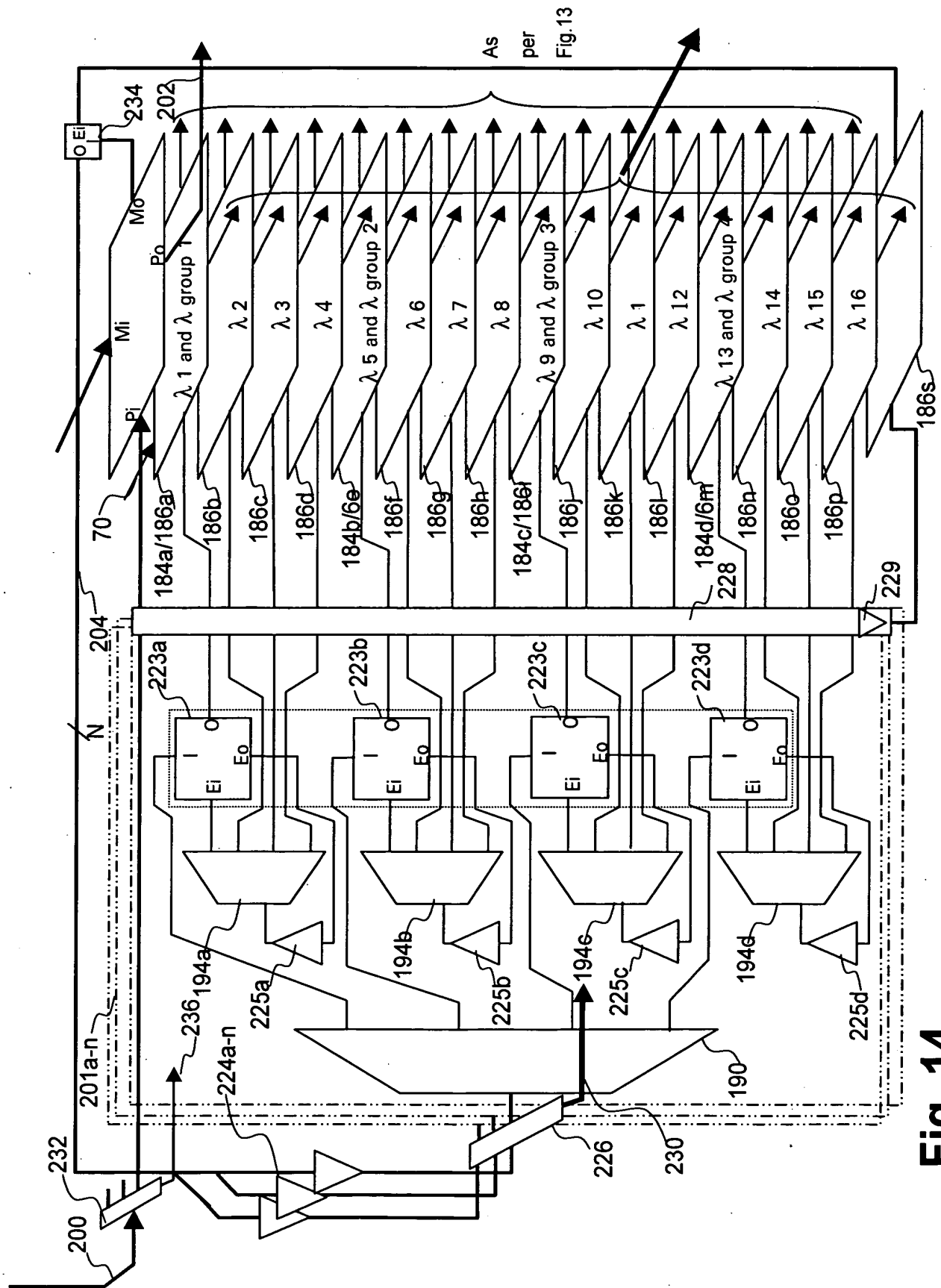


Fig. 14